



Recent Highlights and 2026 Roadmap

contact@realtimeatwork.com

January 7, 2026



Purpose

- Summarize recent product updates
- Present short-term roadmap

RTaW-Pegase™ Modules

ETHERNET TSN	GUI, command-line or JAVA API
<ul style="list-style-type: none"> • Modelling, visualization and design variants • Any physical layer incl. TIS • Simulation & worst-case analysis: 802.1Q, CBS, TAS, preemption, TCP, UDP, TFTP, TTE, CB, DRR, TIS, AS-2020, ATS, PSFP, SOME/IP (TP), DDS • Automated configuration: (redundant) routing, 802.1Q (priorities), CBS (idle slopes), TAS (schedule), TIS (schedule), preemption, ATS, PSFP (traffic policer), combined QoS mechanisms • What-if analysis: scale load, migration from CAN (FD) to TIS • .arxml import 	
CAN / CAN FD / CAN XL / J1939	
<ul style="list-style-type: none"> • Simulation & worst-case analysis • Gatewaying CAN – CAN, CAN – Ethernet • Automated configuration: allocation of IDs & offsets • Traffic models: periodic, sporadic, bursts, diagnostics, segmentation, event-triggered • What-if analysis: migration to CAN FD & CAN 2.0B, scale load • .dbc and .arxml import 	

SOFTWARE-DEFINED X
<ul style="list-style-type: none"> • Hypervisors and RTOS support (e.g., Autosar classic) • Task scheduling: FPS, NPFPS, schedule tables • End-to end delays: sensors → CPU → network(s) → CPU(s) → actuators • Simulation, worst-case analysis, Gantt charts • .arxml import
TSN GENERATIVE DESIGN
<ul style="list-style-type: none"> • Zero-Config TSN ® • Topology Stress Test ® • Topology Optimizer®
TRACE-INSPECTOR
<ul style="list-style-type: none"> • Controller-Area Network • Switched Ethernet and TIS

SOFTWARE-DEFINED X
<ul style="list-style-type: none"> • Hypervisors and RTOS support (e.g., Autosar classic) • Task scheduling: FPS, NPFPS, schedule tables • End-to end delays: sensors → CPU → network(s) → CPU(s) → actuators • Simulation, worst-case analysis, Gantt charts • .arxml import
TSN GENERATIVE DESIGN
<ul style="list-style-type: none"> • Zero-Config TSN ® • Topology Stress Test ® • Topology Optimizer®
TRACE-INSPECTOR
<ul style="list-style-type: none"> • Controller-Area Network • Switched Ethernet and TIS

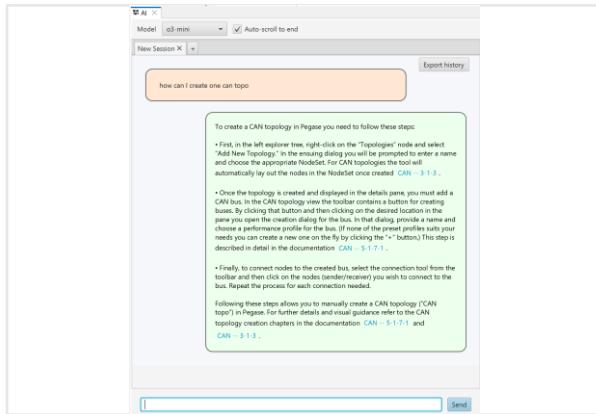
TSN HW DEPLOYMENT
<ul style="list-style-type: none"> • IEEE/IETF compliant YANG models • Deployment over Netconf • TSN devices characteristics import
AI DOCUMENTATION ASSISTANT:
Answers user questions about Pegase and its documentation

TSN HW DEPLOYMENT
<ul style="list-style-type: none"> • IEEE/IETF compliant YANG models • Deployment over Netconf • TSN devices characteristics import
AI DOCUMENTATION ASSISTANT:
Answers user questions about Pegase and its documentation

User-Experience

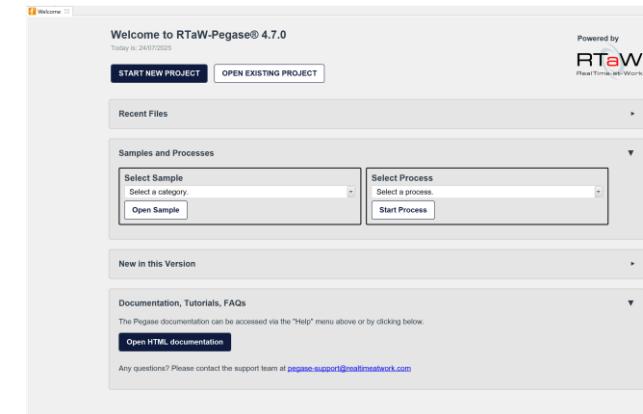
- UX improvement focused on speed and accessibility of features

AI documentation assistant (v4.8)



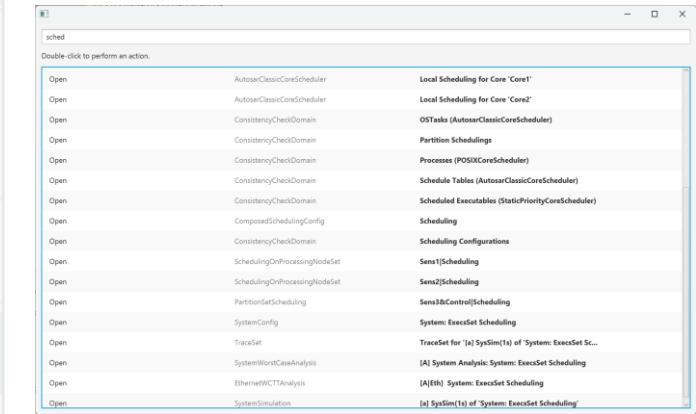
- Ask questions inside the tool
- Step-by-step guidance and links to docs
- Reduces onboarding and "how do I..." time

Welcome page (v4.7)



- Quick access to recent models and samples
- Release notes and help in one place
- Clear support contact entry point

Search everywhere (v4.7)



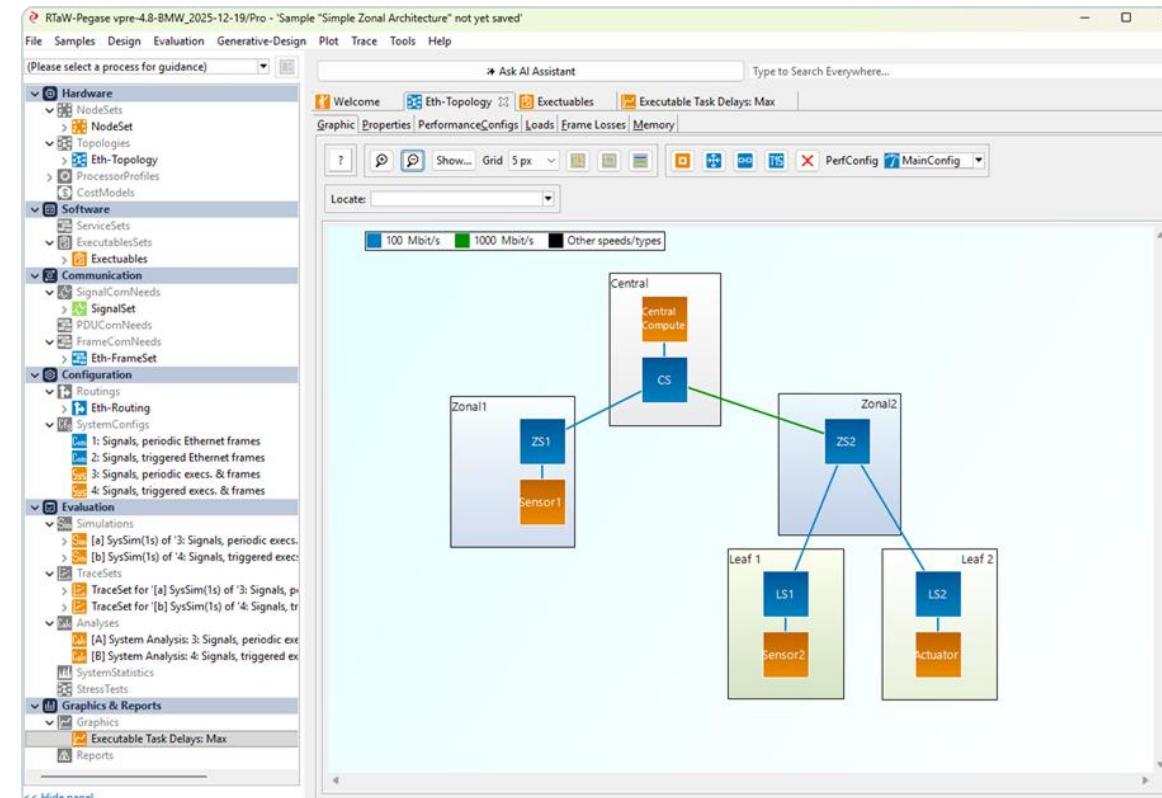
- Shift-Shift / Ctrl-F global search
- Jump to any matching entity
- Supports large models

User-Experience

- UX improvement focused on speed and accessibility of features

Project Tree Redesign (v4.8)

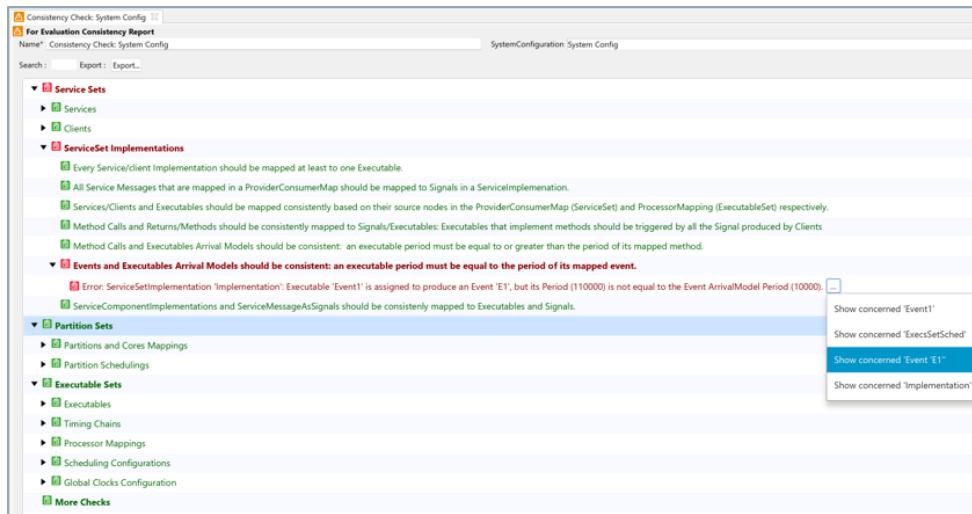
- Reorganized the project tree into clear categories aligned with engineering workflow:
 - Hardware,
 - Application: software, services
 - Communication: signals, PDUs, frames, flows
 - Configuration
 - Evaluation,
 - Reporting
- Faster navigation and match how users build and validate systems.



Pre- and Post-Evaluation Reporting

- Improved reports help validate configurations, identify root causes, and navigate directly to affected entities.

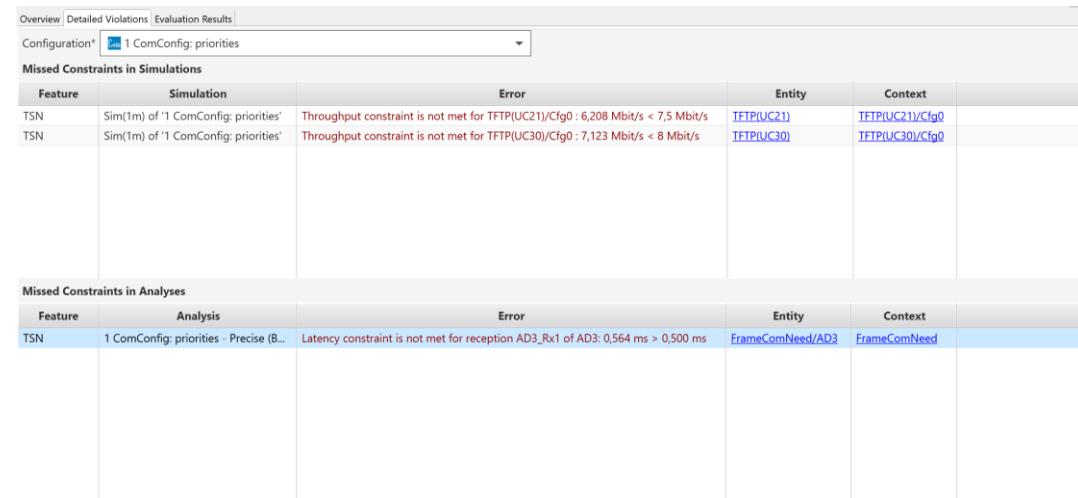
Pre-evaluation report (v4.7)



The screenshot shows a 'Consistency Check System Config' interface. The left sidebar lists 'Service Sets', 'ServiceSet Implementations' (expanded to show rules about mapping and execution), 'Partition Sets', and 'Executable Sets'. The main area displays a list of consistency checks, such as 'ServiceSets should be consistent' and 'Events and Executables Arrival Models should be consistent'. Each check includes an explanatory message and a 'Show concerned' link.

- Checks modeling constraints for completeness/correctness
- Highlights errors with explanatory messages
- One-click access to the issue context

Post-evaluation report (v4.7)



The screenshot shows a 'Post-evaluation report' interface with 'Overview', 'Detailed Violations', and 'Evaluation Results' tabs. The 'Detailed Violations' tab is active, showing 'Missed Constraints in Simulations' and 'Missed Constraints in Analyses' tables. The 'Missed Constraints in Simulations' table lists violations for TSN with 'ComConfig: priorities' across 'Sim(1m)' and 'Throughput constraint is not met' for TFTP(UC21) and TFTP(UC30). The 'Missed Constraints in Analyses' table lists a single violation for TSN with 'ComConfig: priorities - Precise (B...)' and 'Latency constraint is not met' for FrameComNeed/AD3.

- Overview + Violations + Evaluation Results tabs
- Centralized list of issues with context and links
- Quick access to charts and generated results

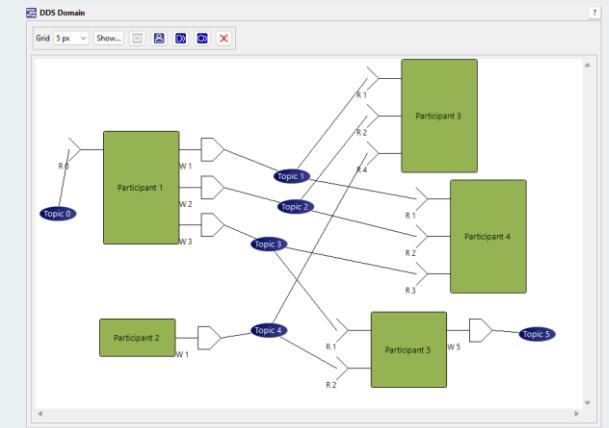
Ethernet TSN

- Improved ARXML support and better PREEvision™ interoperability (export of TSN parameters to ARXML)
- MACsec security support (802.1AE)
- Stream ID support for FRER (802.1CB) and PSFP (802.1Qci)
- Automated configuration for FRER replication and PSFP traffic policing
- Improved DDS™ over TSN workflow, including DDS-TSN XML import (ETA: May 2026)
- Full TTS support, including automated migration from CAN

Customer impact

- Less manual TSN configuration with fewer integration errors
- More comprehensive TSN feature coverage (security, resiliency, policing)
- Better toolchain integration through improved ARXML/PREEvision and DDS workflows

**DDS Domain view
in RTaW-Pegase**



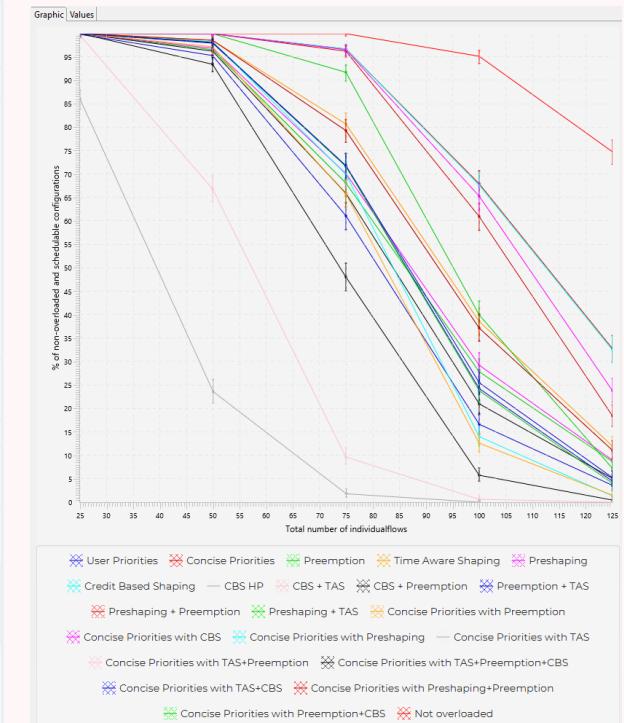
Ethernet Generative Design

- TST: Major User Experience improvements (usability)
- TST: Clearer guidance with richer documentation
- TST: New ATS based scheduling options, incl. ATS+TAS
- TST + ZCT: Additional algorithm-driven priority assignments to improve configuration quality

Customer impact

- Smoother learning curve (clearer docs, improved UX).
- Faster design iterations (improved workflows)
- Higher-quality configurations with less manual tuning

TSN mechanisms: capability to satisfy timing constraints when load increases



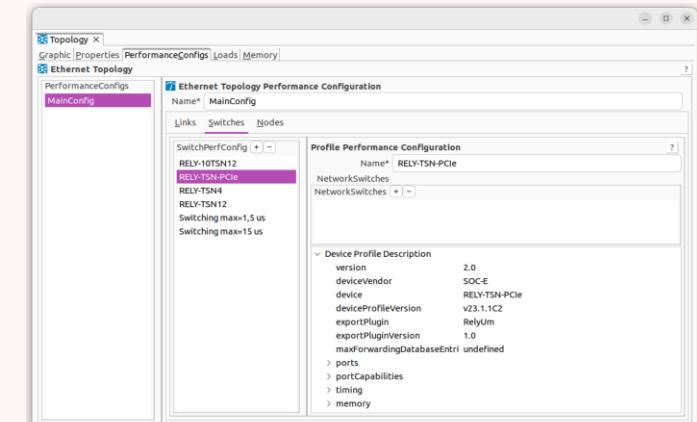
TSN HW Deployment

- Broader built-in device/product coverage
- New device profile description language to support HW heterogeneity : internal + PHY timings, TSN capabilities, memory constraints
- Up-to-date YANG model export compliant with the latest IEEE standards

Customer impact

- Generate TSN configurations that are valid for the target hardware
- Avoid frame losses caused by memory/buffer overflows
- Streamline deployment from model to device, reducing manual steps and integration risk

Pegase Device Profile Editor



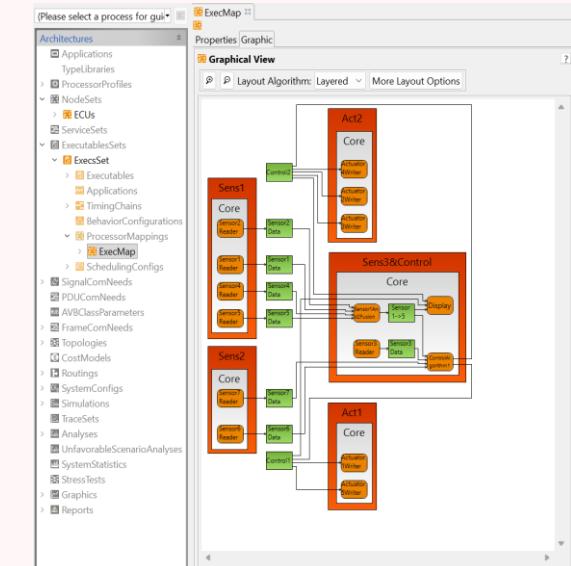
Software-Defined X

- SOME/IP service-induced load evaluation (v4.8)
- POSIX scheduling support (SCHED_*) as used in AUTOSAR Adaptive
- Graphical visualization of task-to-processor mapping
- Automated task-to-processor mapping and scheduling configuration
- Execution traces showing CPU spare capacity (idle time)
- ARXML ECU extract import: runnables, OsTasks, schedules, timing chains (ETA: Mar 2026)

Customer impact

- Earlier performance validation by quantifying SOME/IP-induced CPU/network load before integration.
- Richer scheduling specifications to support modern, adaptive architectures.
- More automation end-to-end: import ECU extracts, auto-map tasks to CPUs, and generate scheduling configurations with minimal manual effort.

Task-to-Processor Mapping View



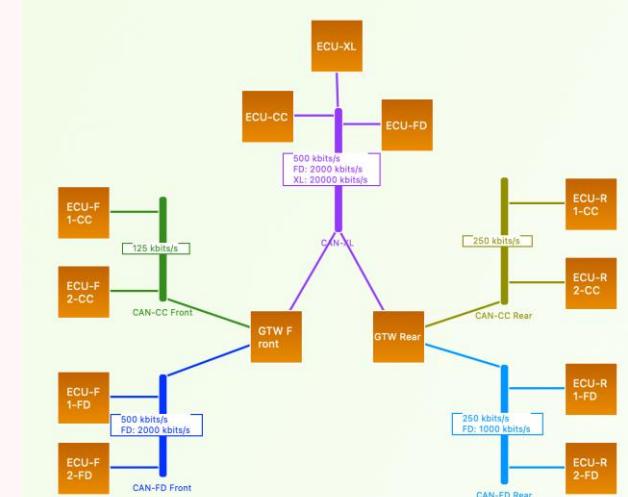
CAN (FD, XL)

- Support for networks with mixed CAN-CC, CAN FD and CAN XL interfaces
- Additional statistics: busy periods, reception jitters
- J1939 Support (ETA: Mar 2026)

Customer impact

- Accurate analysis of mixed CAN networks (CC/FD/XL) without simplifying assumptions.
- Additional capabilities to validate timing behavior
- Expanded functional coverage with upcoming J1939 support, reducing toolchain fragmentation.

Mixed CAN-CC, FD and XL network



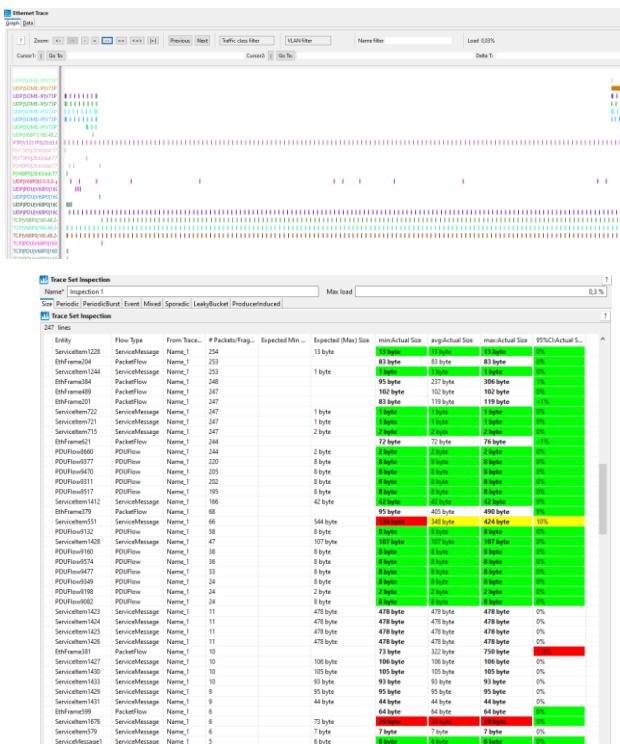
Trace-Inspector

- Import and inspect multiple traces simultaneously
- Improved UX for easy navigation between model, trace view and inspection results
- CAN: Improved analysis for mixed periodic / event-driven frames
- ETH:
 - Import bidirectional recordings
 - Improved trace import report and inspection tables
 - Update the model from traces (services, PDUs, frames) for more realistic simulations

Customer impact

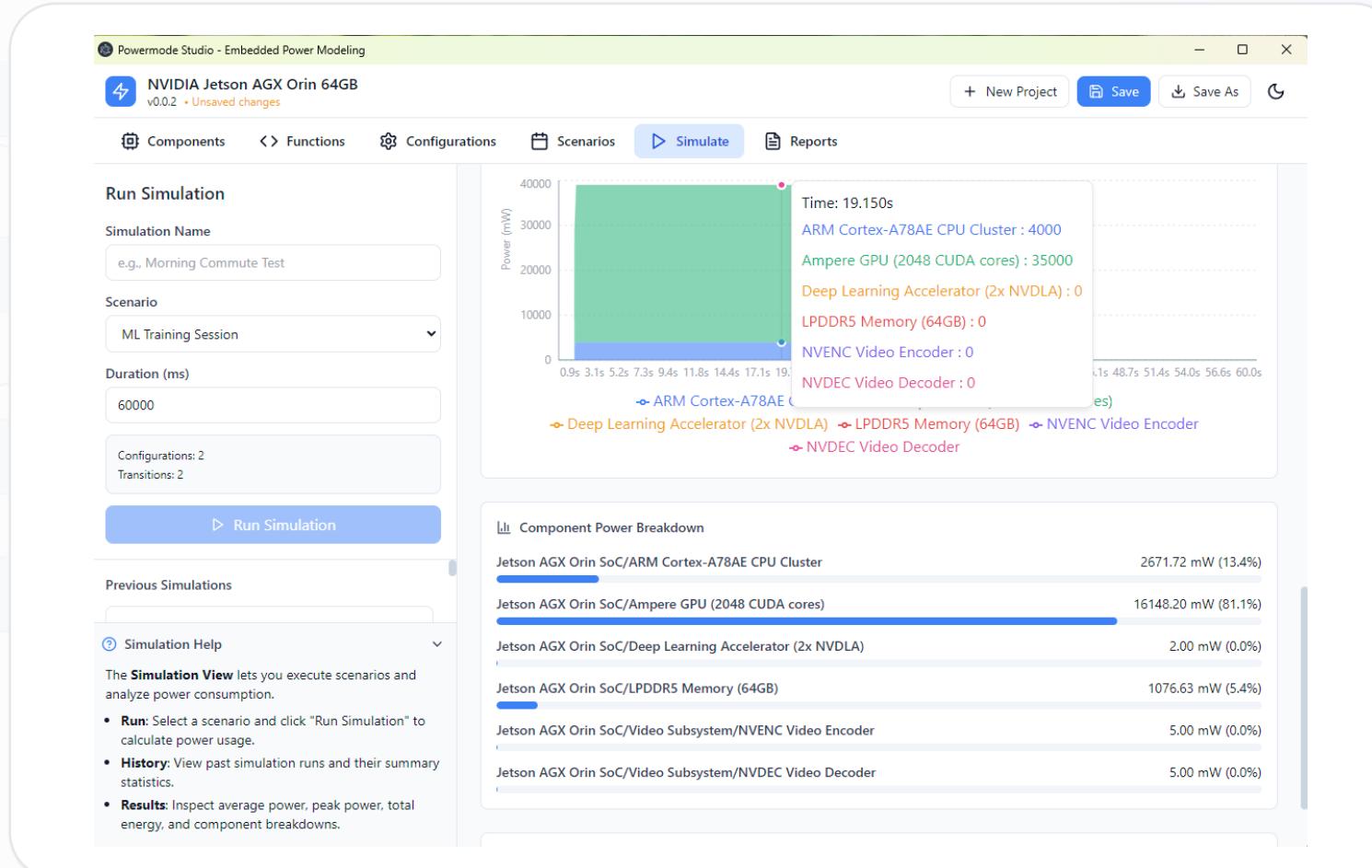
- Inspect traces at a glance to validate both content and timing behavior.
- Detect non-conformances from the implementation and incorrect modeling assumptions.
- Enrich models from traces to reproduce realistic traffic patterns and improve simulation fidelity.

Trace inspection: Gantt chart and inspection results table



New tool: PowerMode Studio

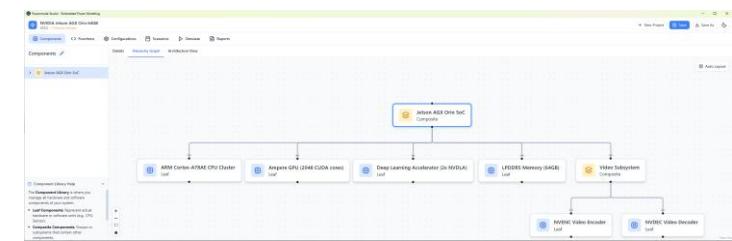
- Model operating states and workload scenarios for compute units and networks, simulate and report peak/average power breakdowns for representative systems.



Key capabilities

- Component library for HW/SW parts
- Operating states (Sleep/Active/Perf)
- Scenario editor (e.g., inference, video)
- Simulation and reports
- Support microcontrollers and SoCs (Nvidia, QualComm)
- Extension to system-level power modeling, including network power

Example: NVIDIA ORIN



Recent Publications

- “[PSFP in TSN Networks: Insights into Some Practical Limitations](#)” — ERTS 2026
- “[Overcoming QoS Challenges in a Full Automotive Ethernet Architecture](#)” — IEEE-SA Ethernet & IP @ Automotive Technology Day 2025 — with Renault / Ampere
- “[Automotive System Requirements on Traffic Shaping](#)” — TSN/A 2024 — with BMW
- “[The Future of In-Vehicle Electronic Architecture Design: Entering the Centaur Era](#)” — China SAE Congress 2024
- “[DDS over TSN: configuring TSN to meet DDS level QoSes](#)” — Automotive Ethernet Congress 2024 — with RTI
- “[ATS vs CBS shaper differences](#)” — TSN/A 2023 — with Ethernovia

Next Steps

- Interested in any of these topics? Schedule a demo focused on the features most relevant to you.
- Missing important features? We welcome your input to help define our roadmap and priorities.

CONTACTS

GENERAL ENQUIRIES
REQUEST AN RTAW-PEGASE
DEMO / PRICING

contact@realtimeatwork.com

CHINA ENQUIRIES

xiaojie.guo@realtimeatwork.com

POWERMODE STUDIO DEMO

TechDemo@realtimeatwork.com

Enabling Correctness by Design

Contact: contact@realtimeatwork.com

